

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Canceled)

2. (Currently Amended) [[A]] The coupling node in a telecommunication system according to claim 4 [[1]], wherein the selector device has information on which resources are required for the handling of the function, and is arranged to compare said resources of the hunted processor with the corresponding resources for the handling of the function.

3. (Currently Amended) [[A]] The coupling node in a telecommunication system according to claim 4 [[1]], wherein the selector device is arranged to hunt for a second one of the processors if said first processor lacks resources for the handling.

4. (Currently Amended) A coupling node in a telecommunication system according to claim 1, A coupling node in a telecommunication system for coupling of communications in the telecommunication system, said coupling node comprising:

connections for the communications and a connection for a server;
function devices with functions intended for the communications, the functions being supported by carriers, wherein the function devices are by the carriers arranged in a hierachic structure;

processors supported by the carriers;

a selector device being arranged to couple, after a signal from the server, at least a first of the functions, wherein the selector device is arranged to, at the coupling of at least one of the functions, on one hand hunt for a first one of the processors being a candidate for handling said at least one of the functions, on the other hand investigate

whether the hunted processor has resources in the form of sufficient free space in its data store and in its instruction memory and sufficient processor capacity for the handling, wherein

the function devices are by the carriers arranged in a hierachic structure:

the coupling node comprises internal communication resources for the function devices;

the function devices each having have their own hierachic address, corresponding to the hierachic structure of the function devices, and the selector device being is arranged, by means of the hierachic addresses to select two of the function devices for one of the communications within the communication consecutive of said functions in such a way that the amount of internal communication resources being utilized for connecting said two function devices, is limited.

5. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support the processors with access points, the selector device being arranged to select the two function devices in one and the same magazine.

6. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support the processors with access points, the selector device being arranged to select the two function devices in one and the same printed board assembly.

7. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the

magazine and which support the processors with access points, the selector device being arranged to select the two function devices in one and the same processor.

8. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support the processors with access points, the selector device being arranged to select the two function devices so that they will have the same access point in one of the signal processors.

9. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support the processors with access points, the selector device being arranged to select the two function devices, in the first place, in the same printed board assembly and, in the second place, in the same magazine.

10. (Currently Amended) [[A]] The coupling node according to claim 4, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support the processors with access points, the selector device being arranged to select the two function devices, in the first place so that they can be reached via the same access point, in the second place so that they can be handled by one and the same processor, in the third place so that they can be handled by the processors in the same printed board assembly, and in the fourth place so that the printed board assemblies are placed in the same magazine.

11. (Currently Amended) [[A]] The coupling node according to claim 5, wherein one of the printed board assemblies supports function devices having only one type of functions.

12. (Currently Amended) [[A]] The coupling node according to claim 5, wherein one of the printed board assemblies supports function devices having at least two different types of functions.

13. (Canceled)

14. (Currently Amended) [[A]] The method for a coupling node according to claim 16 43, further comprising comparing, in the selector device, said resources in the hunted processor with the corresponding resources required for handling the function.

15. (Currently Amended) [[A]] The method for a coupling node according to claim 16 43, further comprising hunting, by the selector device, for a second processor if said first processor lacks resources for the handling.

16. (Currently Amended) A method for a coupling node according to claim 13, A method for a coupling node in a telecommunication system for coupling of a communication, the coupling node comprising connections for communications and a connection for a server; function devices with functions, intended for the communications and supported by carriers; and processors supported by the carriers, the method comprising:

selecting at least one of the functions after a signal from the server;
hunting for a first processor as a candidate for handling of said function; and
investigating whether the hunted processor has sufficiently free space in its data store and in its instruction memory and sufficient processor capacity for said handling,
wherein the coupling node comprises internal communication resources for the function devices; and wherein the method comprises

arranging the function devices in a hierachic structure by means of the carriers for the function devices;

allocating to each of the function devices a hierachic address, corresponding to the hierachic structure of the function devices; and

selecting, by means of the hierachic addresses, two of the function devices for one of the communications within the communication consecutive of said functions in such a way that the amount of internal communication resources being utilized for connecting said two function devices, is limited.

17. (Currently Amended) [[A]] The method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points, the method comprising selection of the two function devices in the same magazine.

18. (Currently Amended) [[A]] The method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points. the method comprising selection of the two function devices in the same printed board assembly.

19. (Currently Amended) [[A]] The method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points, and wherein the method comprises selecting the two function devices in the same signal processor.

20. (Currently Amended) [[A]] The A method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points, and wherein the method

comprises selecting the two function devices so that they will have the same access point in one of the processors.

21. (Currently Amended) [[A]] The A method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points, and wherein the method comprises selecting the two function devices, in the first place, in the same printed board assembly and, in the second place, in the same magazine.

22. (Currently Amended) [[A]] The method according to claim 16, wherein the carriers for the function devices comprise at least one magazine with printed board assemblies, which are connected to each other via a back plane in the magazine and which support processors with access points, and wherein the method comprises selecting the two function devices, in the first place so that they are reached via the same access point, in the second place so that they are handled by the same processor, in the third place so that they are handled by processors in the same printed board assembly, and in the fourth place so that the printed board assemblies are placed in the same magazine.

23. (Currently Amended) [[A]] The method according to claim 17, wherein one of the printed board assemblies supports function devices having only one type of functions.

24. (Currently Amended) [[A]] The A method according to claim 17, wherein one of the printed board assemblies supports function devices having at least two different types of functions.